


<b>Search Notes</b>  	<b>Application/Control No.</b>  09989777	<b>Applicant(s)/Patent Under Reexamination</b>  NEMECEK, CRAIG
	<b>Examiner</b>  Jacob, Mary C	<b>Art Unit</b>  2123

SEARCHED			
Class	Subclass	Date	Examiner
703	23,26	1/22/08	MCJ
716	224	1/22/08	MCJ
714	28	1/22/08	MCJ

SEARCH NOTES		
Search Notes	Date	Examiner
Updated Inventor Name Search: EAST, IEEE, Google Scholar	1/22/08	MCJ
Assignee Name Search: EAST, Google Scholar, IEEE	1/22/08	MCJ
Updated East Search: see search history	1/22/08	MCJ
Google Scholar Search: cypress semiconductor and sleep or stall, clocks; emulation FPGA sleep stall clock	1/22/08	MCJ
IEEE Search: (sleep or stall) and (off near4 clock); DUT and (off near4 clock); emulation and (off near4 clock); see search printouts	1/22/08	MCJ

INTERFERENCE SEARCH			
Class	Subclass	Date	Examiner